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INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Application Number	10/530420
	Filing Date	
	First Named Inventor	Andrei Radulescu
	Art Unit	2-155
	Examiner Name	Kim
	Attorney Docket Number	NL03 0771 US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number No.-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS						
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/EJK/		KUMAR S ET AL: "A NETWORK ON CHIP ARCHITECTURE AND DESIGN METHODOLOGY" VLSI ON ANNUAL SYMPOSIUM, IEEE COMPUTER SOCIETY ISVLSI 2002 PITTSBURGH, PA, USA, 25-26 APRIL 2002, LOS AALMITOS, CA, IEEE COMPUT. SOC., US 25 APRIL 2002	
/EJK/		CHU-SING YANG ET AL: "BUILDING AN ADAPTABLE, FAULT TOLERANT, AND HIGHLY MANAGEABLE WEB SERVER ON CLUSTERS OF NON-DEDICATED WORKSTATIONS" IEEE, 21 AUGUST 2000, PAGES 413-420	

Examiner Signature	/Edward J Kim/	Date Considered	02/12/2008
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